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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/679,794	10/06/2003	Michael L. Babala	1-24754	3701
4859	7590 10/28/2004		EXAM	INER
	AN SOBANSKI & TO	CHAPMAN JR, JOHN E		
	MARITIME PLAZA FOURTH FLOOR ATER STREET		ART UNIT	PAPER NUMBER
TOLEDO, OH 43604-1619			2856	

DATE MAILED: 10/28/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/679,794	BABALA, MICHAEL L.				
Office Action Summary	Examiner	Art Unit				
	John E Chapman	2856				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a rep If NO period for reply is specified above, the maximum statutory period  - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailine earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be timply within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from e, cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).				
Status	•					
1) Responsive to communication(s) filed on 24 S	September 2004.					
·— ·	·					
3) Since this application is in condition for allowa	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
<ul> <li>4)  Claim(s) 11-17 and 20-24 is/are pending in the application.</li> <li>4a) Of the above claim(s) is/are withdrawn from consideration.</li> <li>5)  Claim(s) is/are allowed.</li> <li>6)  Claim(s) 11-14,20,23 and 24 is/are rejected.</li> <li>7)  Claim(s) 15-17,21 and 22 is/are objected to.</li> <li>8)  Claim(s) are subject to restriction and/or election requirement.</li> </ul>						
Application Papers	·					
9) The specification is objected to by the Examiner.						
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>						
Attachment(s)  1) Notice of References Cited (PTO-892)	4) Interview Summary	(PTO-413)				
<ul> <li>2) Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date</li> </ul>	Paper No(s)/Mail D  5) Notice of Informal F  6) Other:	ate Patent Application (PTO-152)				

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## **DETAILED ACTION**

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after allowance or after an Office action under *Ex Parte Quayle*, 25 USPQ 74, 453 O.G. 213 (Comm'r Pat. 1935). Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, prosecution in this application has been reopened pursuant to 37 CFR 1.114. Applicant's submission filed on September 24, 2004 has been entered.

- 2. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
- 3. Claims 14 and 24 are rejected under 35 U.S.C. 103(a) as being unpatentable over Condne et al. in view of Greiff et al.

Condne discloses an inertial sensor comprising a sensor element 20 mounted on a base member 16, and a plurality of signal conditioning circuits 36 and 40 connected to the sense element. High frequency and low frequency accelerations comprise different ranges. Hence, the only difference between the claimed invention and the prior art consists in mounting the sense element on a silicon wafer. Greiff teaches that it is known in the art to mount an accelerometer on a silicon wafer in order to provide a micromechanical accelerometer. Accordingly, merely to mount the sense element of Condne on a silicon wafer would have been obvious to one having ordinary skill in the art in order to provide a micromechanical accelerometer.

Claims 11-14, 23 and 24 are rejected under 35 U.S.C. 103(a) as being unpatentable over 4. Broillet et al. in view of Greiff et al.

Broillet discloses an inertial sensor comprising an acceleration sensor 31 mounted on a body, which is subjected to vibrations, and a plurality of signal conditioning circuits 33 and 34 connected to the accelerometer. Low frequency vibrations comprise a first range of change in a body motion parameter, and high frequency vibrations comprise a second range of change in a body motion parameter. Hence, the only difference between the claimed invention and the prior art consists in mounting the sense element on a silicon wafer. Greiff teaches that it is known in the art to mount an accelerometer on a silicon wafer in order to provide a micromechanical accelerometer. Accordingly, merely to mount the acceleration sensor of Broillet on a silicon wafer would have been obvious to one having ordinary skill in the art in order to provide a micromechanical accelerometer.

Regarding claims 11 and 13, it is well known in the art to provide signal conditioning circuitry integral with a silicon wafer in order to provide a compact sensor, as well as separately therefrom in order to provide interchangeability of different signal conditioning circuitry with a single sensor.

Claim 20 is rejected under 35 U.S.C. 103(a) as being unpatentable over Broillet in view 5. of Greiff as applied to claim 12 above, and further in view of Flash et al.

Flash teaches that it is known in the art to form an application specific integrated circuit 14 on a silicon wafer 10. Accordingly, it would have been obvious to one having ordinary skill Application/Control Number: 10/679,794 Page 4

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in the art to include the signal conditioning circuits integral within an application specific integrated circuit formed on a silicon wafer.

6. Claims 15-17, 21 and 22 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to John E Chapman whose telephone number is (571) 272-2191. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hezron Williams can be reached on (571) 272-2208. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

John E Chapman Primary Examiner